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TITLE: Method for turning-on computer capable of debugging system and computer system thereof

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PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
CN 1170221 C	October 6, 2004	N/A	000	G06F 009/445
CN 1405677 A	March 26, 2003	N/A	000	G06F 009/445

APPLICATION-DATA:

PUB-NO	APPL-DESCRIPTOR	APPL-NO	APPL-DATE
CN 1170221C	N/A	2002CN-0151358	November 18, 2002
CN 1405677A	N/A	2002CN-0151358	November 18, 2002

INT-CL (IPC): G06F009/445, G06F011/28, G06F011/34, G06F013/38

ABSTRACTED-PUB-NO: CN 1405677A

BASIC-ABSTRACT:

NOVELTY - The system comprises the CPU, the system bus, the peripheral bus, the expansion bus, the south bridge and the north bridge, the ROM and debug system. The ROM connected to the expansion bus stores the program codes of the BIOS. The debug system is connected to the peripheral bus. The invention includes following steps. The CPU is switched to the normal mode, sending the data request from the system bus to the ROM. The CPU is switched to the debug mode, sending the data request from the system bus to the debug system. The north bridge transfers the data request in the system bus to the peripheral bus. Base on the position pointed by the data, the south bridge or debug system loads the BIOS to the CPU.

CHOSEN-DRAWING: Dwg.0/0

TITLE-TERMS: METHOD TURN COMPUTER CAPABLE DEBUG SYSTEM COMPUTER SYSTEM

DERWENT-CLASS: T01

EPI-CODES: T01-F01B; T01-F05B; T01-G05A; T01-G05C1; T01-H07;

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